

Installation taps and splitters

1.8 GHz Core Series Vertical Graded 6-way Multitap

technetix

Notes

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| 1 | Point to point linear limit line |
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| 2 | Deviation over operational temperature range: +/-0.5 dB insertion loss. +2 dB isolation and return loss |
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| 3 | Deviation over extended operational temperature: +/-1 dB insertion loss. +5 dB Isolation and return loss |
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| 4 | Two carriers (60 & 65 MHz), out to out, @ 120 dBuV, fully demagnetized |
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| 5 | Two carriers (60 & 65MHz), out to out, out to tap (worst case), @ 120 dBuV, after 10 pulses (25V/1.2uS rise time/500uS duration) at all ports. |
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| 6 | Two carriers (60 & 65 MHz), out to out, out to tap (worst case), @ 120 dBuV, after 1x positive and 1x negative pulses (1kV/1.2uS rise time / 50uS fall time) at all ports. |
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| 7 | Surge pulse 1kV/1.2uS rise time / 50uS fall time (IEC61000-4-5:1995) 2 Ω source impedance (1x positive and 1x negative) |
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| 8 | IEC 62153-7 § 5.5, IEC 60728-2 and EN-50083 (transfer impedance method, absorbing clamp) |
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Ordering information

Item name	Article number
CTVZ-6-16/21	19014656
